

Kick-off Meeting

Workshop on AI for Semiconductor Processing

Integrated Virtual-Physical AI for Ultra-Precision
Manufacturing of Advanced Semiconductor Devices

February 4 - 6, 2026 | Building 102, Room 310 | UNIST, Ulsan, South Korea

Principal Investigators

Prof. Satoshi Hamaguchi
Osaka Univ.

Prof. Stephen Wu
ISM Tokyo

Prof. Byungjo Kim
UNIST

Day 1: Feb 4, 2026 (Wed)

10:00-10:30	Welcome & Project Introduction (Prof. B. Kim)
10:30-12:00	VDLab Introduction / Campus Tour
12:00-13:30	Lunch (Boxed Meal)
13:30-14:30	Prof. Hamaguchi Seminar: Atomic-scale Analysis of Plasma-surface Interaction
14:30-14:45	Coffee Break
14:45-15:30	VDLab Student Presentation (1)
15:30-16:15	VDLab Student Presentation (2)
16:15-17:00	Open Discussion & Q&A
18:00-20:00	Welcome Dinner: Unyang Giwajip Bulgogi

Day 2: Feb 5, 2026 (Thu)

09:30-10:00	Preparation for UCRF Tour
10:00-12:00	UCRF Tour (UNIST Central Research Facilities)
12:00-13:30	Lunch (Boxed Meal)
13:30-14:30	Prof. Stephen Wu Seminar: LLM-based Agentic AI Applications in Science and Engineering
14:30-14:45	Coffee Break
14:45-17:00	Collaborative Research Discussion: MLFF, Digital Twin, Student Exchange
18:00-20:00	Farewell Dinner: Dodong Sanbang

Day 3: Feb 6, 2026 (Fri)

09:00-11:00	Free Time / Individual Meetings
11:00-12:00	Wrap-up Meeting & Action Items
After Lunch	Departure or Free Schedule

Dinner Venues Information

Day 1: Unyang Giwajip Bulgogi (Traditional Korean BBQ)
Day 2: Dodong Sanbang (Traditional Korean Cuisine)



Day 1 Map



Day 2 Map

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